

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. M122-1689		SERIAL NO. D9/848846	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Luan C. Tran		GROUP Filed herewith	
					FILING DATE Filed herewith 5/3/01		GROUP Filed herewith	
U.S. PATENT DOCUMENTS								
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
AA	4,612,629	09/16/86	Harari					
AB	4,785,342	11/15/88	Yamanaka et al.					
AC	5,786,249	07/28/98	Dennison					
AD	6,074,924	06/13/00	Dennison et al.					
AE	5,272,367	12/21/93	Dennison et al.					
AF	6,004,854	12/21/99	Dennison et al.					
AG	5,683,927	11/04/97	Dennison et al.					
AH	6,124,616	09/26/00	Dennison et al.					
AI	5,747,855	05/05/98	Dennison et al.					
AJ	5,534,449	07/09/96	Dennison et al.					
AK	5,7706,806	07/07/98	Dennison et al.					
AL	5,773,863	06/60/98	Burr et al.					
FOREIGN PATENT DOCUMENTS								
	Document Number	Date	Country	Class	Subclass	Translation		
						Yes	No	
AM	EP 0 717 448 A1	06/19/96	EPO					
AN	WO 98/29897	07/09/98	PCT					
AO								
AP								
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
AQ	Silicon Processing for the VLSI Era, Vol. III; "Punchthrough (Subsurface-DIBL) in Short-Channel MOSFETs"; undated; pp. 232-235, 238-244							
AR								
AS								
EXAMINER Luan C. Tran				DATE CONSIDERED 10/17/02				
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>								

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